

These release notes for the Reed-Solomon (RS) Compiler version 7.1 contain the following information:

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System Requirements

The RS Compiler has the same system requirements as the Quartus® II software.



For system requirements and installation instructions, refer to *Quartus II Installation & Licensing for Windows* or *Quartus II Installation & Licensing for UNIX & Linux Workstations*.

New Features & Enhancements

The following list outlines new features and enhancements in this release:

- Full support for Stratix® II GX devices
- Full support for HardCopy® II devices
- Preliminary support for Arria™ GX devices



For more information on Avalon Streaming interfaces, refer to the *Avalon Streaming Interface Functional Specification*.

Errata Fixed in This Release

The VHDL designs do not simulate in Synopsys VCS errata was fixed in this release.



For existing up-to-date errata, refer to the *Reed-Solomon Compiler v7.1 Errata Sheet* on the Altera® website at www.altera.com/literature/es/es_rs_71.pdf.

Contact Altera

Although every effort has been made to ensure that this version of the RS Compiler works correctly, if problems occur, use the following contact information to communicate issues to the appropriate Altera representative.

For technical support or other information about Altera products, go to the Altera world-wide website at www.altera.com. You can also contact Altera through your local sales representative or any of the sources listed in [Table 1](#).

| Information Type | USA & Canada | All Other Locations |
|-------------------------------|--|--|
| Technical support | www.altera.com/mysupport/ | www.altera.com/mysupport/ |
| | 800-800-EPLD (3753) 7:00 a.m. to 5:00 p.m. Pacific Time | +1 408-544-8767 7:00 a.m. to 5:00 p.m. (GMT -8:00) Pacific Time |
| Product literature | www.altera.com | www.altera.com |
| Altera literature services | literature@altera.com | literature@altera.com |
| Nontechnical customer service | 800-767-3753 | + 1 408-544-7000 7:00 a.m. to 5:00 p.m. (GMT -8:00) Pacific Time |
| FTP site | ftp.altera.com | ftp.altera.com |

Revision History [Table 2](#) shows the revision history for the RS Compiler.

| Version | Date | Revision |
|---------|---------------|---|
| 7.1 | May 2007 | See “New Features & Enhancements” on page 1 |
| 7.0 | December 2006 | Support for Cyclone® III devices |
| 6.1 | December 2006 | <ul style="list-style-type: none"> ● Support for Stratix® III devices ● Improved RS decoder: ● Uses less memory ● Hides variable latency output from decoder operating in continuous mode ● Avalon® Streaming interfaces |
| 4.1.0 | April 2006 | Maintenance release. |
| 4.0.1 | November 2005 | Fixed issue with synthesis for OpenCore Plus hardware evaluation. |
| 4.0.0 | October 2005 | <ul style="list-style-type: none"> ● Preliminary support for Stratix II GX and HardCopy® II devices ● Atlantic™ interface ● Single architecture decoder ● Support for 2 to 128 check symbols ● Support for 3 to 12 bits per symbol ● Customized VHDL or Verilog HDL testbench ● Customized Tcl script for simulation ● Error symbol output ● Bit error output—either split count or full count |

Table 2. Reed-Solomon Compiler Revision History

| Version | Date | Revision |
|---------|------|---|
| 3.6.0 | – | Improvements to erasures-supporting option. |
| 3.5.0 | – | <ul style="list-style-type: none"> ● Support for Cyclone II devices ● DSP Builder ready |
| 3.4.0 | – | <ul style="list-style-type: none"> ● Support for Stratix II devices ● Support for IP toolbench ● IP functional simulation models for use in Altera-supported VHDL and Verilog HDL simulators. ● No support for DSP Builder ● Increased maximum frequency for standard continuous decoder |



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