



Introduction

These release notes for the HyperTransport MegaCore Function v7.0 contain the following information:

- [New Features & Enhancements](#)
- [Errata Fixed in This Release](#)
- [Contact Altera](#)
- [Revision History](#)

For system requirements and installation instructions, refer to *Quartus II Installation & Licensing for Windows* or *Quartus II Installation & Licensing for UNIX & Linux* on the Altera website:

www.altera.com/literature/lit-qts.jsp

New Features & Enhancements

This release contains no new features.

Errata Fixed in This Release

No errata were fixed in this maintenance release.

For existing up-to-date errata, refer to the *HyperTransport MegaCore Function v7.0 Errata Sheet* on the errata page on the Altera website:

www.altera.com/literature/es/es_hypertransport_70.pdf

Contact Altera

Although every effort has been made to ensure that this version of the HyperTransport MegaCore Function works correctly, if problems occur, use the following contact information to communicate issues to the appropriate Altera representative.

For technical support or other information about Altera products, go to the Altera world-wide website at www.altera.com. You can also contact Altera through your local sales representative or any of the sources listed in [Table 1](#).

Table 1. Contacting Altera

Information Type	USA & Canada	All Other Locations
Technical support	www.altera.com/mysupport/	www.altera.com/mysupport/
	800-800-EPLD (3753) 7:00 a.m. to 5:00 p.m. Pacific Time	+1 408-544-8767 7:00 a.m. to 5:00 p.m. (GMT -8:00) Pacific Time
Product literature	www.altera.com	www.altera.com
Altera literature services	literature@altera.com	literature@altera.com
Nontechnical customer service	800-767-3753	+ 1 408-544-7000 7:00 a.m. to 5:00 p.m. (GMT -8:00) Pacific Time
FTP site	ftp.altera.com	ftp.altera.com

Revision History

Table 2 shows the revision history for the HyperTransport MegaCore Function v7.0.

Table 2. HyperTransport MegaCore Function Revision History

Version	Date	Revision
7.0	December 2006	<ul style="list-style-type: none"> Maintenance release
6.1	December 2006	<ul style="list-style-type: none"> Maintenance release
1.4.0	October 2005	<ul style="list-style-type: none"> Two new system signals to better handle PLL reset. Support for Stratix II GX
1.3.2	February 2004	<ul style="list-style-type: none"> Addition of I/O Space BAR capability Addition of ability to claim requests with the Compat bit set
1.3.1	December 2004	<ul style="list-style-type: none"> Addition of the new inputs RxPllAreset_i and TxPllAreset_i Fix for Available Buffer Credit tracking issue Fix for HT link initialization issue when the incoming sync phase (when CAD and CTL are all 1) of the initialization sequence was at the minimum of 16 bit times Change to remove Quartus fitter warnings about inputs to the altlvds_tx SERDES input
1.3.0		<ul style="list-style-type: none"> Preliminary support for the Stratix II device family. This included adding a new parameter, intended_device_family, for ht_e8. The intended_device_family parameter must match the device_family targeted by the design. Support for HyperTransport clock frequencies of 500 MHz (1.0 Gbps per bit data rate) in Stratix II. Support for IP Functional Simulation Models. Support for OpenCore® Plus hardware evaluation.



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