

These release notes for the DDR and DDR2 SDRAM High-Performance Controller MegaCore functions version 7.1 SP1 contain the following information:

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## System Requirements

The DDR and DDR2 SDRAM High-Performance Controller has the same system requirements as the Quartus® II software.

When migrating from the Quartus II software v7.1 to v7.1 SP1, Altera recommends that you regenerate any existing MegaCore function variations and SOPC Builder systems in development to ensure that they use the latest version of each core. Quartus II recompilation of the existing project is not sufficient to update the core version.



For system requirements and installation instructions, refer to *Quartus II Installation & Licensing for Windows* or *Quartus II Installation & Licensing for UNIX & Linux Workstations*.

## New Features & Enhancements

The following list outlines new features and enhancements in this release.

- Maintenance release

## Errata Fixed in This Release

No errata were fixed in this release.



For existing up-to-date errata, refer to the *DDR and DDR2 SDRAM High-Performance Controller v7.1 SP1 Errata Sheet* on the Altera® website at [www.altera.com/literature/es/es\\_ddr\\_ddr2\\_sdram\\_hp\\_71.pdf](http://www.altera.com/literature/es/es_ddr_ddr2_sdram_hp_71.pdf).

## Contacting Altera

Although every effort has been made to ensure that this version of the DDR and DDR2 SDRAM High-Performance Controller works correctly, if problems occur, use the following contact information to communicate issues to the appropriate Altera representative.

For technical support or other information about Altera products, go to the Altera world-wide website at [www.altera.com](http://www.altera.com). You can also contact Altera through your local sales representative or any of the sources listed in [Table 1](#).

Information Type	USA & Canada	All Other Locations
Technical support	<a href="http://www.altera.com/mysupport/">www.altera.com/mysupport/</a>	<a href="http://www.altera.com/mysupport/">www.altera.com/mysupport/</a>
	800-800-EPLD (3753) 7:00 a.m. to 5:00 p.m. Pacific Time	+1 408-544-8767 7:00 a.m. to 5:00 p.m. (GMT -8:00) Pacific Time
Product literature	<a href="http://www.altera.com">www.altera.com</a>	<a href="http://www.altera.com">www.altera.com</a>
Altera literature services	<a href="mailto:literature@altera.com">literature@altera.com</a>	<a href="mailto:literature@altera.com">literature@altera.com</a>
Nontechnical customer service	800-767-3753	+ 1 408-544-7000 7:00 a.m. to 5:00 p.m. (GMT -8:00) Pacific Time
FTP site	<a href="ftp://ftp.altera.com">ftp.altera.com</a>	<a href="ftp://ftp.altera.com">ftp.altera.com</a>

## Revision History

[Table 2](#) shows the revision history for the DDR and DDR2 SDRAM High-Performance Controller.

Version	Date	Revision
7.1 SP1	June 2007	See <a href="#">“New Features &amp; Enhancements”</a> on page 1.
7.1	May 2007	<ul style="list-style-type: none"> <li>• Preliminary support for HardCopy® II devices</li> <li>• Preliminary support for Arria™ GX devices</li> <li>• Full-rate support for Cyclone® III, HardCopy II, Stratix® II, and Stratix II GX devices</li> <li>• Power-up on-chip termination (OCT) support for Cyclone III and Stratix III devices</li> <li>• Integrated error correction coding (ECC) function</li> <li>• SOPC Builder ready</li> </ul>
7.0	December 2006	Preliminary support for Cyclone III devices.
6.1	December 2006	First release.



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