



## 6. Reference & Ordering Information

SII51006-2.2

### Software

Stratix® II devices are supported by the Altera® Quartus® II design software, which provides a comprehensive environment for system-on-a-programmable-chip (SOPC) design. The Quartus II software includes HDL and schematic design entry, compilation and logic synthesis, full simulation and advanced timing analysis, SignalTap® II logic analyzer, and device configuration. See the *Quartus II Handbook* for more information on the Quartus II software features.

The Quartus II software supports the Windows XP/2000/NT/98, Sun Solaris, Linux Red Hat v7.1 and HP-UX operating systems. It also supports seamless integration with industry-leading EDA tools through the NativeLink® interface.

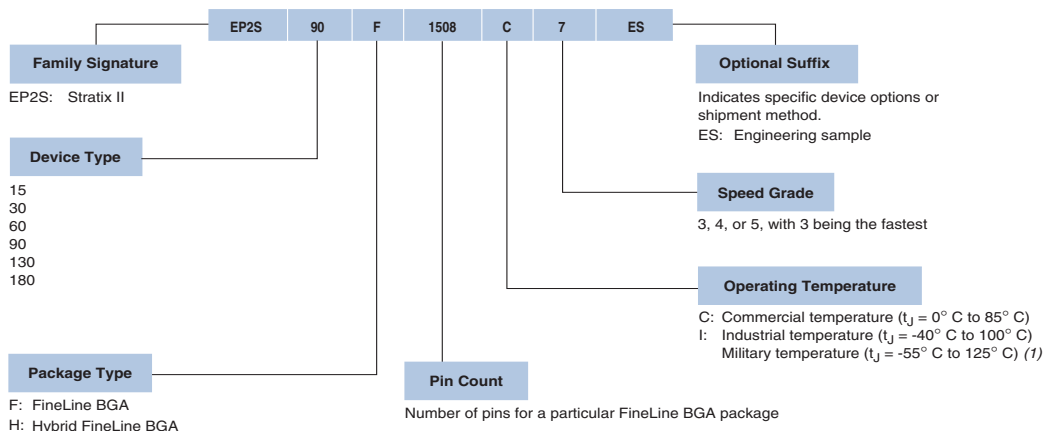
### Device Pin-Outs

Device pin-outs for Stratix II devices are available on the Altera web site at ([www.altera.com](http://www.altera.com)).

### Ordering Information

[Figure 6-1](#) describes the ordering codes for Stratix II devices. For more information on a specific package, refer to the *Package Information for Stratix II & Stratix II GX Devices* chapter in volume 2 of the *Stratix II Device Handbook* or the *Stratix II GX Device Handbook*.

**Figure 6–1. Stratix II Device Packaging Ordering Information**



**Note to Figure 6–1:**

- (1) Applicable to 14 devices. For more information, refer to the *Stratix II Military Temperature Range Support* technical brief.

## Document Revision History

Table 6–1 shows the revision history for this chapter.

<b>Date and Document Version</b>	<b>Changes Made</b>	<b>Summary of Changes</b>
April 2011, v2.2	Updated Figure 6–1.	Added operating junction temperature for military use.
May 2007, v2.1	Moved the Document Revision History section to the end of the chapter.	—
January 2005, v2.0	Contact information was removed.	—
October 2004, v1.1	Updated Figure 6–1.	—
February 2004, v1.0	Added document to the Stratix II Device Handbook.	—